Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	24	"programmable gate array" same icon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:17
L2	29	"programmable gate array" same icon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:18
L3	10355	"programmable gate array" same (programming or reprogramming or configur\$5 or reconfigur\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:19
L4 .	2856	"programmable gate array" near5 (programming or reprogramming or configur\$5 or reconfigur\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/11/16 16:19
L5	0	"programmable gate array" near5 (programming or reprogramming or configur\$5 or reconfigur\$5 ) same icon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:20
L6	172	"programmable gate array" near5 (programming or reprogramming or configur\$5 or reconfigur\$5 ) and gui	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:20
L7	392	717/109.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:42
L8	782	717/168.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ,	OFF	2006/11/16 16:42
L9	15	717/109.ccls. and "gate array"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:42

L10	20	717/168.ccls. and "gate array"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:42
L11	31	717/109.ccls. and fpga and configur\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:42
L12	14	717/168.ccls. and fpga and configur\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ^	OFF	2006/11/16 16:43
L13	40	l9 l10 l11l 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:43
L14	14	(I9 I10 I11I 12 ) and (gui or ui or icon\$6 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:43
S1	214	717/125.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 16:41
S2 ·	309	717/106.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:16
S3	47	("4849880" "4901221" "5109504" "5437464" "5497498" "5535342" "5555201" "5566295" "5583749" "5603043" "5652875" "5684980" "5732277" "5784275" "5847955" "6219628" "6226776" "6230307" "6311149" "5541849" "5737235" "5638299" "5309556" "6064409" ).pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2005/06/24 14:47
S4	527	716/17.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2005/06/23 16:36

S5	838	716/18.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:36
S6	1133	716/6.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:37
S7	1148	716/1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:37
S8	1037	716/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2005/06/23 16:37
S9	492	716/3.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:37
S10	1800	716/4.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:39
S11	709	715/700.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 16:40
S12	264	715/763.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2005/06/23 17:02
S13	3	(associative and measurement).as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 17:35
S14	15	(reconfigurable and test and system). ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 17:36

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S15	19	("4849880"   "4901221"   "5109504"   "5437464"   "5497498"   "5535342"   "5566295"   "5583749"   "5603043"   "5652875"   "5684980"   "5784275"   "5847955").PN. OR ("6311149").URPN.	US-PGPUB; USPAT; USOCR	OR .	OFF	2005/06/24 12:19
S16	1	"6219628".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:24
S17	147976	(highlight\$3 or (high near2 light\$3))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:25
S18	1779	(highlight\$3 or (high near2 light\$3) ) same (ui or gui or "user interface") and icon	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:34
S19	13	(highlight\$3 or (high near2 light\$3) ) same (ui or gui or "user interface") and icon and "graphical development"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:26
S20	12	("5237691"   "5517645"   "5642511"   "5745712"   "5760788"   "5805896"   "5850548"   "6014138"   "6016392"   "6226787"   "6298474"   "6385769"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:28
S21	3	(("5237691"   "5517645"   "5642511"   "5745712"   "5760788"   "5805896"   "5850548"   "6014138"   "6016392"   "6226787"   "6298474"   "6385769"). PN. ) and icon\$6 near3 (highlight\$3 or modify\$3 or modification or indicat\$3 )	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 13:32
S22	3	(("5237691"   "5517645"   "5642511"   "5745712"   "5760788"   "5805896"   "5850548"   "6014138"   "6016392"   "6226787"   "6298474"   "6385769"). PN. ) and icon\$6 near3 (highlight\$3 or modify\$3 or modification or indicat\$3 or chang\$3 or alter\$5 )	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:30
S23	23	("4462046"   "4679137"   "4759074"   "4833624"   "4914567"   "5005119"   "5157663"   "5314055"   "5327350"   "5383110"   "5453933"   "5481712"   "5511147"   "5574828"   "5742504"   "5862372"   "5905649"   "5920479"   "5933353"   "5933638"   "5940296"   "6061602"   "6064759").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:31

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S24	2	(("4462046"   "4679137"   "4759074"   "4833624"   "4914567"   "5005119"   "5157663"   "5314055"   "5327350"   "5383110"   "5453933"   "5481712"   "5511147"   "5574828"   "5742504"   "5862372"   "5905649"   "5920479"   "5933353"   "5933638"   "5940296"   "6061602"   "6064759").PN. ) and icon\$6 near3 (highlight\$3 or modify\$3 or modification or indicat\$3 or chang\$3 or alter\$5 )	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:34
S25	9	(ui or gui or "user interface") and icon and (specify near3 (clock\$3 or cycle or timing or trigger\$3 or events or interrupts or traps) ) and (graphical\$2 adj development)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:56
S26	0	"6219628.pn",	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 12:58
S27	0	"6219628.pn."	US-PGPUB; USPAT; USOCR	OR	OFF .	2005/06/24 12:59
S28	0	"6219628.pn."	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 13:00
S29	2	("6219628").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/24 13:00
S30	1	"6311149".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 13:50
S31	11	("4849880" "4827427" "4922432" "4967367" "4970664" "5051938" "5084824" "5164911" "5197016" "5210699" "5036473").pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 13:52
S32	2	("6181143" ).pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/24 14:47
S33	4	("6311149" "6608638").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:55

S34	493	schematic near4 library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:56
S35	118	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:57
S36	120	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" or dac or adc )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:57
S37	0	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" or dac or adc ) same icon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:58
S38	0	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" or dac or adc ) and icon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:58
S39	120	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" or dac or adc )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:58
S40	41	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" or dac or adc ) and graphic\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 16:58
S41	35	(schematic near4 library ) and (counter or timer or "a/d converter" or "signal conditioning" or dac or adc ) and graphic\$4 and (programmable or reprogrammable or configur\$4 or reconfigur\$4 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/13 08:11
S42	10	("5541849" "5737235" "5638299" "5309556" "6064409" ).pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/13 08:27

S43	4	("6219628" "5005119").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/13 08:27
S44	2	"6219628".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/16 09:17
S45	2	"5005119".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/16 10:35
S46	7	grape same "data flow"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/16 10:36
S47	2	"4901224".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF ·	2006/05/24 10:54
S48	2	"4901221".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/24 10:54
S49	3554	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) and (flash\$3 same peripheral)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 08:35
S50	253	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) same (flash\$3 same peripheral)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 07:52
S51	3	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) same (flash\$3 same peripheral) and (button or icon ) near5 peripheral	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON ·	2006/11/16 07:56

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S52	12	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) same (flash\$3 same (peripheral or firmware) ) and ((button or icon ) near5 (peripheral or firmware) )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 07:57
S53	2319	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) and (flash\$3 or reprogram\$5 or rio) and (measurement or oscilloscope or "control card" or daq) same (counter or adc or port or filter or timer or ai or "a/d" or ao or (signal adj condition\$3 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2006/11/16 08:42
S54	31	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) and (flash\$3 or reprogram\$5 or rio) near5 (measurement or oscilloscope or "control card" or daq) same (counter or adc or port or filter or timer or ai or "a/d" or ao or (signal adj condition\$3 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 08:42
S55		(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) and (flash\$3 or reprogram\$5 or rio) near5 (measurement or oscilloscope or "control card" or daq) same (counter or adc or port or filter or timer or ai or "a/d" or ao or (signal adj condition\$3 ) ) not (national).as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 08:49
S56	18	(gui or ui or "user interface" or ((visual or graphical or icon ) adj (display or monitor or input or screen or program\$4 ) ) ) and (chang\$3 or modify\$3 or modification or edit\$3 or alter\$5 or program\$4 or flash\$3 or reprogram\$5 or rio) near5 ( "control card" or daq) same (counter or adc or port or filter or timer or ai or "a/d" or ao or (signal adj condition\$3 ) ) not (national).as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 09:47

S57	1	"5907491".pn. and (gui or ui or "user interface" or (user near3 input\$4 ) )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/16 09:48
S58	55	("5907491").URPN.	USPAT	OR	OFF	2006/11/16 09:48
S59	28	("5907491").URPN. and (gui or ui or "user interface" or (user near3 input\$4 ))	USPAT	OR	OFF	2006/11/16 09:53
S60	383	sensor near3 (icon or button) and (gui or ui or "user interface" or (user near3 input\$4 ) )	USPAT	OR .	OFF	2006/11/16 10:02
S61	5	S49 and S60	USPAT	OR	OFF	2006/11/16 09:54
S62	62	sensor near3 (icon or button) and (gui or ui or "user interface" or (user near3 input\$4 ) or "command console" ) near3 (icon or button)	USPAT	OR	OFF	2006/11/16 10:03



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1 Architectural and physical design challenges for one-million gate FPGAs a

Jonathan Rose, Dwight Hill

February 1997 Proceedings of the 1997 ACM fifth international sympos programmable gate arrays

**Publisher:** ACM Press

Full text available: pdf(620.75 Additional Information: full citation, reference KB)

KB)

index terms

2 Bridging fault detection in FPGA interconnects using IDDQ

L. Zhao, D. M. H. Walker, F. Lombardi

March 1998 Proceedings of the 1998 ACM/SIGDA sixth international s Field programmable gate arrays

**Publisher:** ACM Press

Full text available: pdf(1.02 Additional Information: full citation, abst citings, index ten

This paper presents a vector generation approach for testing interconnect (SRAM-based) Field Programmable Gate Arrays (FPGAs). The propose bridging faults and is based on quiescent current (IDDQ monitoring. Cor previous voltage-based methods, IDDQ testing has the advantage of utili

of programming phases for configuring the FPGA during the test process

3 The Cricket location-support system

Nissanka B. Priyantha, Anit Chakraborty, Hari Balakrishnan

August 2000 Proceedings of the 6th annual international conference on and networking

**Publisher:** ACM Press

Full text available: pdf(1.22 Additional Information: full citation, abst citings

This paper presents the design, implementation, and evaluation of Cricke support system for in-building, mobile, location-dependent applications. applications running on mobile and static nodes to learn their physical lo listeners that hear and analyze information from beacons spread through Cricket is the result of several design goals, including user privacy, deceadministrat ...

- 4 A universal client for distributed networked design and computing
- Franc Brglez, Hemang Lavana

June 2001 Proceedings of the 38th conference on Design automation Publisher: ACM Press

Full text available: pdf(205.58 Additional Information: full citation, abst index terms, reviews.

We introduce a universal client (OmniFlow) whose GUI can be readily c user to invoke any number of applications, concurrently or sequentially, network. The design and the implementation of the client is based on the taskflow-oriented programming, whereby we merge concepts from struc hardware description, and mark-up languages. A mark-up language such well-defined schema that captures the dec ...

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- Asim Smailagic, Dan Siewiorek

July 1999 ACM SIGMOBILE Mobile Computing and Communication 3 Issue 3

**Publisher:** ACM Press

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- 1 Architectural and physical design challenges for one-million gate FPGAs a
- Jonathan Rose, Dwight Hill

February 1997 Proceedings of the 1997 ACM fifth international sympos programmable gate arrays

**Publisher:** ACM Press

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2 Bridging fault detection in FPGA interconnects using IDDQ

L. Zhao, D. M. H. Walker, F. Lombardi

March 1998 Proceedings of the 1998 ACM/SIGDA sixth international s Field programmable gate arrays

**Publisher:** ACM Press

Full text available: pdf(1.02 Additional Information: full citation, abst citings, index ten

This paper presents a vector generation approach for testing interconnect (SRAM-based) Field Programmable Gate Arrays (FPGAs). The propose bridging faults and is based on quiescent current (IDDQ monitoring. Corprevious voltage-based methods, IDDQ testing has the advantage of utili

number of programming phases for configuring the FPGA during the tes

3 upFRONT

November 1999 Linux Journal

Publisher: Specialized Systems Consultants, Inc.

Full text available: html(44.71 KB) Additional Information: full citation, inde

4 Tools for application-oriented performance tuning

John Mellor-Crummey, Robert Fowler, David Whalley

June 2001 Proceedings of the 15th international conference on Superco Publisher: ACM Press

Full text available: pdf(397.34 Additional Information: full citation, abst citings, index ten

Application performance tuning is a complex process that requires assen of information and correlating it with source code to pinpoint the causes bottlenecks. Existing performance tools don't adequately support this prodimensions. We discuss some of the critical utility and usability issues for performance analysis tools in the context of two performance tools, *MH*! that we built to support our ...

5 Programmable applications: interpreter meets interface

Michael Eisenberg

April 1995 ACM SIGCHI Bulletin, Volume 27 Issue 2

**Publisher:** ACM Press

Full text available: pdf(4.42 Additional Information: full citation, abst terms

Current fashion in "user-friendly" software design tends to place an over manipulation interfaces. To be truly expressive (and thus truly user-frien need both learnable interfaces and domain-enriched languages that are at This paper discusses some of the design issues that arise in the creation of programmable applications. As an example, we present "SchemePaint," application that combines a MacPaint-like interface ...

6 Software/modelware tutorials II: Extend: the Extend simulation environme

David Krahl

December 2000 Proceedings of the 32nd conference on Winter simulati Publisher: Society for Computer Simulation International

Full text available: pdf(616.78 Additional Information: full citation, abst KB) citings

The Extend modeling environment provides an integrated structure for b models and developing new simulation tools. This environment supports modelers on a wide range of levels. Model builders can use Extend's pre components to quickly build and analyze systems without programming. developers can use Extend's built-in, compiled language, ModL to developence. All of this is done within a single, self-contained software.

- 7 Simulation modeling and optimization using ProModel
- Rochelle N. Price, Charles R. Harrell
  December 1999 Proceedings of the 31st conference on Winter simulatio
  bridge to the future Volume 1

**Publisher:** ACM Press

Full text available: pdf(194.45 Additional Information: full citation, reference KB) index terms

- 8 Simulation modeling and optimization using ProModel
- Deborah Benson

December 1997 Proceedings of the 29th conference on Winter simulation Publisher: ACM Press

Full text available: pdf(764.57 KB) Additional Information: full citation, reference.

9 Issues in embedded DRAM development and applications

D. Keitel-Schulz, N. Wehn

December 1998 Proceedings of the 11th international symposium on Sy Publisher: IEEE Computer Society

Full text available: pdf(1.32

MB) Additional Information: <u>full citation</u>, <u>reference</u>

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10 Simulation modeling and optimization using ProModel

Deborah L. Heflin, Charles R. Harrell

December 1998 Proceedings of the 30th conference on Winter simulation

**Publisher:** IEEE Computer Society Press

Full text available: Pdf(313.91 Additional Information: full citation, reference KB)

KB)

index terms

11 How the "what" becomes the "how"

Edward A. Feigenbaum

May 1996 Communications of the ACM, Volume 39 Issue 5

**Publisher:** ACM Press

Full text available: pdf(212.48 Additional Information: full citation, refe

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1. JHDL-an HDL for reconfigurable syste Bellows, P.: Hutchings, B.: FPGAs for Custom Computing Machines, Symposium on 15-17 April 1998 Page(s):175 - 184 Digital Object Identifier 10.1109/FPGA.1 AbstractPlus | Full Text: PDF(60 KB) Il Rights and Permissions

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P Graham

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E Laprise

FPGA Design Tutorial - group of 3 »

S Kittitornkun, CR Kime - ECE554-Digital Engineering

Laboratory, Fall, 2001 - homepages.cae.wisc.edu

... actual commands reflecting what you did to the GUI and

some ... corresponding hardware

configuration bit stream (\*.bit) for programming the FPGA

is generated. ...

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## LOGICAL HARDWARE DEBUGGERS FOR FPGA-

BASED SYSTEMS - group of 4 »

PS Graham - 2001 - splish.ee.byu.edu

... hardware debugger. With this background, Chapter 5 will discuss how FPGA

configuration readback, a device feature, can be leveraged ...

<u>Cited by 10 - Related Articles - View as HTML - Web Search - Library Search</u>

OpenDesign: an open user-configurable project environment forcollaborative design and execution on ... - group of 6 » H Lavana, F Brglez, R Reese, G Konduri, A ... - Computer Design, 2000. Proceedings. 2000 International ..., 2000 - ieeexplore.ieee.org

... VHDL), (2) analyze file, (3) synthesize an **FPGA** device (as ... by writing a simple

configuration file and a set ... line script rather than the nominal GUI, our demos ...

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# IMAGE WAVELET COMPRESSION IMPLEMENTATION USING A RUN-TIME RECONFIGURABLE CUSTOM

#### COMPUTING MACHINE - group of 2 »

Z Ding - 2000 - scholar.lib.vt.edu

... Keywords: Image Wavelet Compression, Janus, FPGA, RTRC, CCM ... Janus' GUI to control the application's Configuring the platform during the

the application's ... Configuring the platform during the execution phase is ...

Related Articles - View as HTML - Web Search

256-Channel Bidirectional Optical Interconnect Using VCSELs and Photodiodes on CMOS - group of 8 »

DV Plant, MB Venditti, E Laprise, J Faucher, K ... JOURNAL OF LIGHTWAVE TECHNOLOGY, 2001 ieeexplore.ieee.org

... A graphical user interface (GUI), written in Visual C ... PRBS on the bus connecting

the FPGA to the ... chip and did not represent the final optical configuration. ...

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## [BOOK] <u>Heterogeneous Concurrent Modeling and Design in Java - group of 5 »</u>

J Davis... - 1998 - ptolemy.eecs.berkeley.edu
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CONCURRENT MODELING AND DESIGN IN JAVA
John Davis, II Christopher Hylands Bart Kienhuis Edward ...
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## [BOOK] <u>Information Technology Encyclopedia and Acronyms</u>

E Kajan - 2001 - books.google.com ... Examples include Al, ATM, CAE, CAPI, CIM, COM, CORBA, C/S, DNS, DVB, DW, EC, EIS, EJB, FDDI, FTP, FTTC, GPS, GUI, HPCC, HTML, HTTP, 12-DSI, Internet, IP, ISDN ...

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Automatic mapping of graphical programming applications to microelectronic technologies - group of 2 »

SW Ong - 2001 - vlsi1.engr.utk.edu
... The

rsttypeofintegratedcircuitistheprogrammablelogicdevices such as Field Programmable

Gate Array (**FPGA**) and Complex Programmable Logic Device (CPLD). ...

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WR Hix, FK Thielemann, AM Khokhlov, JC Wheeler - 1997 - adsabs.harvard.edu

Title: Simplifying silicon burning: Application of quasiequilibrium to (alpha)

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